



FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER LLP

Two Freedom Square ■ 11955 Freedom Drive ■ Reston, VA 20190-5675 ■ 571.203.2700 ■ Fax 202.408.4400
www.finnegan.com

RICHARD V. BURGUJIAN
571.203.2790
Rich.Burgujian@finnegan.com

August 29, 2003

ATTORNEY DOCKET NO. 04329.3128
CUSTOMER NO. 22,852



Mail Stop Patent Application

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

New U.S. Patent Application
Title: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME
Inventor's Name and Residence:

Katsuhiko HIEDA
Yokohama-shi, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 32 pages, including 3 independent claims and 12 claims total.
2. Drawings- 14 sheets of formal drawings containing 27 figures (Figures 1A-15).
3. Certified copy of Japanese Application No. 2003-103043, filed April 7, 2003.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with 1 document attached.
5. The filing fee is calculated as follows:

Basic Application Filing Fee					\$750	\$ \$750.00
	Number of Claims		Basic	Extra Claims		
Total Claims	12	-	20		x \$18	
Independent Claims	3	-	3		x \$84	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 750.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 750.00

6. A check for \$750.00 is enclosed for the filing fee.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Application No. 2003-103043, filed April 7, 2003.

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow,
Garrett & Dunner, L.L.P.
1300 I Street, N.W.
Washington, D.C. 20005-3315

Please accord this application an application number and filing date.

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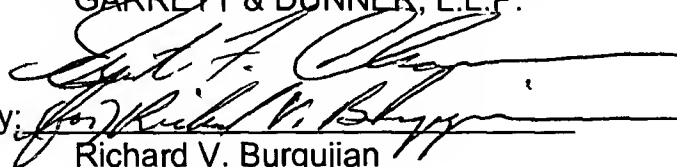
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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By:



Richard V. Burgujian
Reg. No. 31,744

RVB/FPD/gah
Enclosures

ERNEST F. CHAPMAN
Reg. No. 25,961